

File 347: JAPI O Dec 1976-2007/Dec(Updated 080328)

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File 350: Derwent WPI X 1963-2008/UD=200831

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Set	Items	Description
S1	21582	SOC OR SIP OR ASIC OR FPSLIC OR SYSTEM(1W) (CHIP OR PACKAGE) OR APPLICATION() SPECIFIC() INTEGRATED() CIRCUIT OR FIELD() PROGRAMMABLE() SYSTEM() LEVEL
S2	1119977	PROCESSOR? ? OR MICROPROCESSOR? ? OR (PROCESSING OR PROCESS)() (UNIT? ? OR DEVICE? ? OR MODULE? ? OR LOGIC OR SYSTEM OR - CORE? ? OR BLOCK? ?) OR CPU? ? OR MPU? ? OR ALU? ? OR ARITHMETIC() LOGIC() UNIT? ?
S3	162195	ROM OR PROM OR EPROM OR EEPROM OR FPO OR EDO OR NVM OR (" - NOT" OR NON)() VOLATILE OR NONVOLATILE) (1W) (MEMORY OR STOR???)
S4	3103	S3(5N) (TEST??? OR VERIFY? OR DEBUG???)
S5	3237250	CHIP? ? OR MICROCHIP? ? OR CIRCUIT? ? OR IC
S6	110587	S2(10N) S5
S7	25624	S3(10N) S5
S8	13	S1 AND S4 AND S6 AND S7
S9	192	S4 AND S6 AND S7
S10	65081	S5(5N) (INDEPENDENT OR DIFFERENT OR SEPARATE)
S11	151273	(SECOND OR 2ND OR ANOTHER OR OTHER) (3W) S5
S12	41	S9 AND S10: S11
S13	37	S12 NOT S8
S14	34	S13 AND PY=1963:2003
S15	31	S13 AND AY=1963:2003 AND AC=US
S16	36	S14: S15

File 348: EUROPEAN PATENTS 1978-2007/ 200819

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File 349: PCT FULLTEXT 1979-2008/ UB=20080508| UT=20080501

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Set	Items	Description
S1	151635	SOC OR SIP OR ASIC OR FPSLIC OR SYSTEM(1W( CHIP OR PACKAGE) OR APPLICATION() SPECIFIC() INTEGRATED() CIRCUIT OR FIELD() PROG- RAMMABLE() SYSTEM() LEVEL
S2	465269	PROCESSOR? ? OR MICROPROCESSOR? ? OR ( PROCESSING OR PROCES- S)() (UNIT? ? OR DEVICE? ? OR MODULE? ? OR LOGIC OR SYSTEM OR - CORE? ? OR BLOCK? ?) OR CPU? ? OR MPU? ? OR ALU? ? OR ARITHME- TIC() LOGIC() UNIT? ?
S3	226599	ROM OR PROM OR EPROM OR EEPROM OR FPO OR EDO OR NVM OR (" - NOT" OR NON)() VOLATILE OR NONVOLATILE) (1W( MEMORY OR STOR???)
S4	2874	S3(5N) (TEST??? OR VERIFY? OR DEBUG???)
S5	835943	CHIP? ? OR MICROCHIP? ? OR CIRCUIT? ? OR IC
S6	103449	S2(10N) S5
S7	29743	S3(10N) S5
S8	169	S4(100N) S6(100N) S7
S9	7	S1/ TI, AB AND S8
S10	29	S1(100N) S4(100N) S6(100N) S7
S11	30	S9: S10
S12	23	S11 AND PY=1978: 2003
S13	17	S11 AND (AC=US OR AC=US/ PR) AND AY=1978: 2003
S14	24	S12: S13
S15	24	IDPAT (sorted in duplicate/non-duplicate order)
S16	66044	S5(5N) (INDEPENDENT OR DIFFERENT OR SEPARATE)
S17	103712	(SECOND OR 2ND OR ANOTHER OR OTHER) (3W) S5
S18	776	S6(20N) S16: S17(20N) S7
S19	17	S4(100N) S18
S20	12	S19 NOT S11

File 8: Ei Compendex(R) 1884-2008/May V2  
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File 62: SPI N(R) 1975-2008/Apr V2  
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File 239: Mathsci 1940-2008/May  
(c) 2008 American Mathematical Society

Set	Items	Description
S1	319017	SOC OR SIP OR ASIC OR FPSLIC OR SYSTEM(1W(CHIP OR PACKAGE) OR APPLICATION(SPECIFIC) INTEGRATED(CIRCUIT OR FIELD) PROGRAMMABLE(SYSTEM) LEVEL
S2	695641	PROCESSOR? ? OR MICROPROCESSOR? ? OR (PROCESSING OR PROCESS)(UNIT? ? OR DEVICE? ? OR MODULE? ? OR LOGIC OR SYSTEM OR CORE? ? OR BLOCK? ?) OR CPU? ? OR MPU? ? OR ALU? ? OR ARITHMETIC( LOGIC) UNIT? ?
S3	399043	ROM OR PROM OR EPROM OR EEPROM OR FPO OR EDO OR NVM OR ("NOT" OR NON) VOLATILE OR NONVOLATILE(1W(MEMORY OR STORAGE))
S4	1948	S3(5N) (TEST??? OR VERIFY? OR DEBUG???)
S5	2392116	CHIP? ? OR MICROCHIP? ? OR CIRCUIT? ? OR IC
S6	36099	S5(5N) (INDEPENDENT OR DIFFERENT OR SEPARATE)
S7	18653	(SECOND OR 2ND OR ANOTHER OR OTHER) (3W S5
S8	104253	S2(10N) S5
S9	7698	S3(10N) S5
S10	0	S1 AND S8 AND S9 AND S6: S7 AND S4
S11	22	S1 AND S6: S9 AND S4
S12	8375	S1(5N) (TEST??? OR VERIFY? OR DEBUG???)
S13	22	S12 AND S2 AND S3
S14	40	S11 OR S13
S15	34	RD (unique items)
S16	20	S15 NOT PY=2003: 2008